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Fig. 1

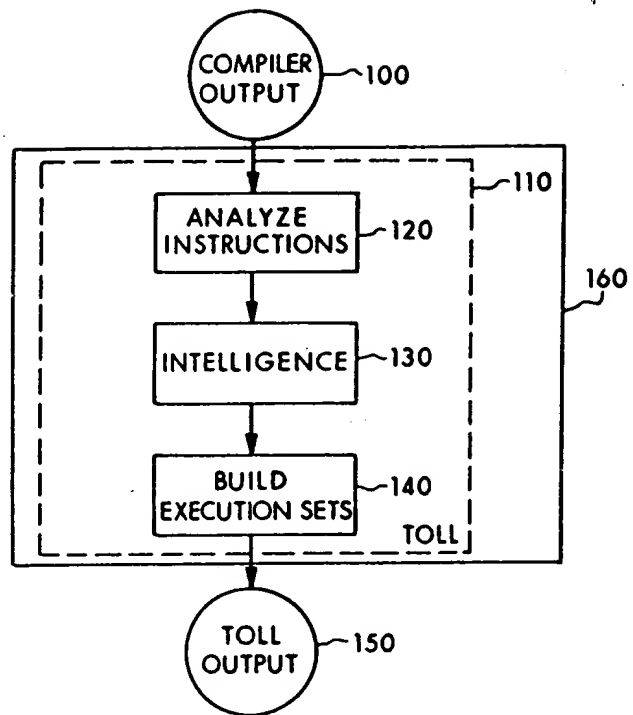


Fig. 2
Prior Art

Basic Block ₁
BB ₂
BB ₃
BB ₄
BB ₅
⋮
BB _{n-1}
BB _n

BB ₁	EXT ₁
BB ₂	EXT ₂
BB ₃	EXT ₃
BB ₄	EXT ₄
BB ₅	EXT ₅
⋮	⋮
BB _{n-1}	EXT _{n-1}
BB _n	EXT _n

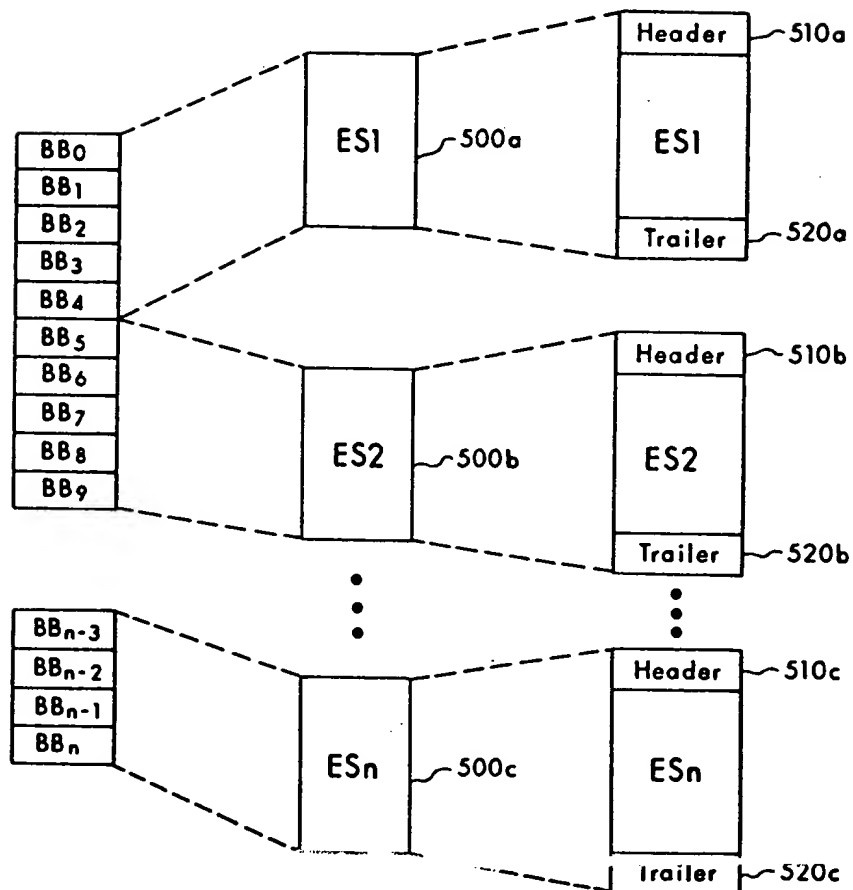
Fig. 3

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Fig. 4

IO	LPN ₀	IFT ₀	SCSM ₀
I1	LPN ₁	IFT ₁	SCSM ₁
⋮			
I _n	LPN _n	IFT _n	SCSM _n

Fig. 5



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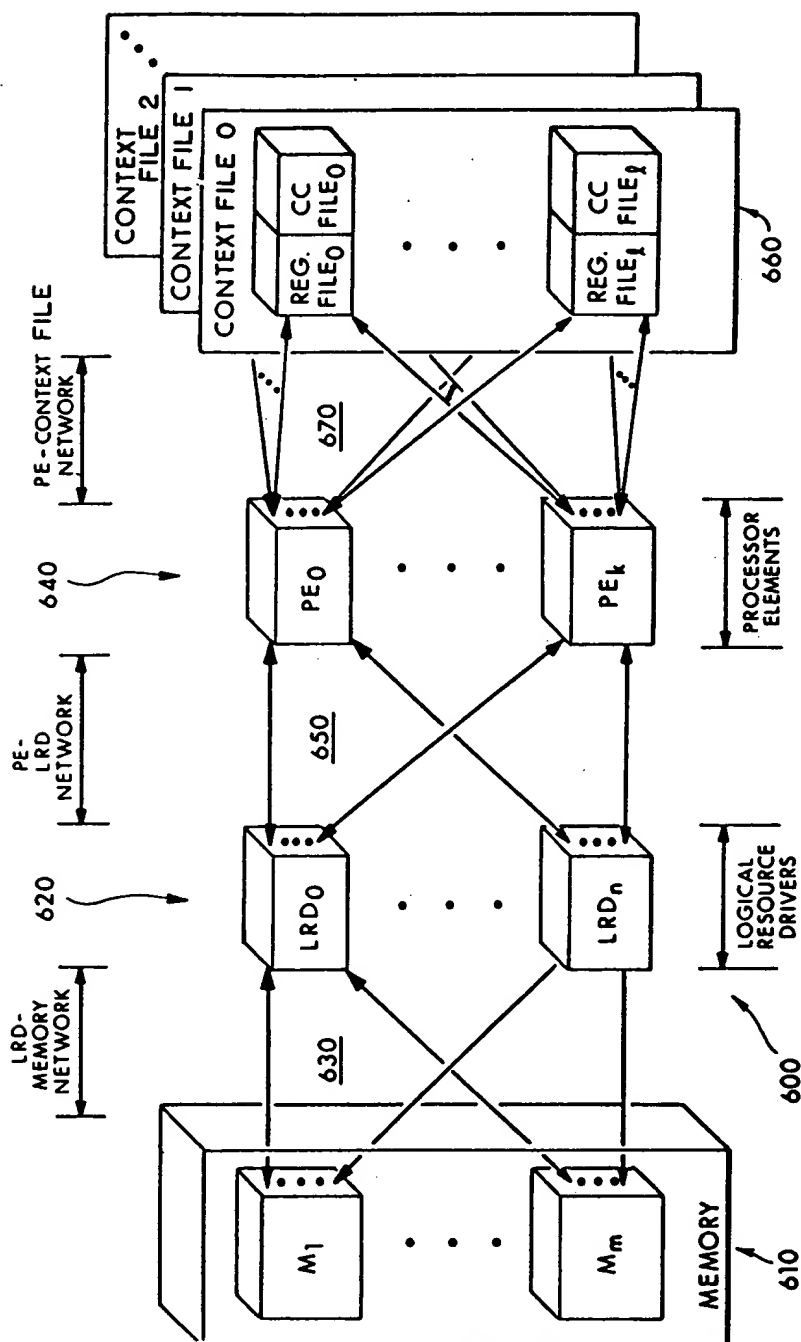
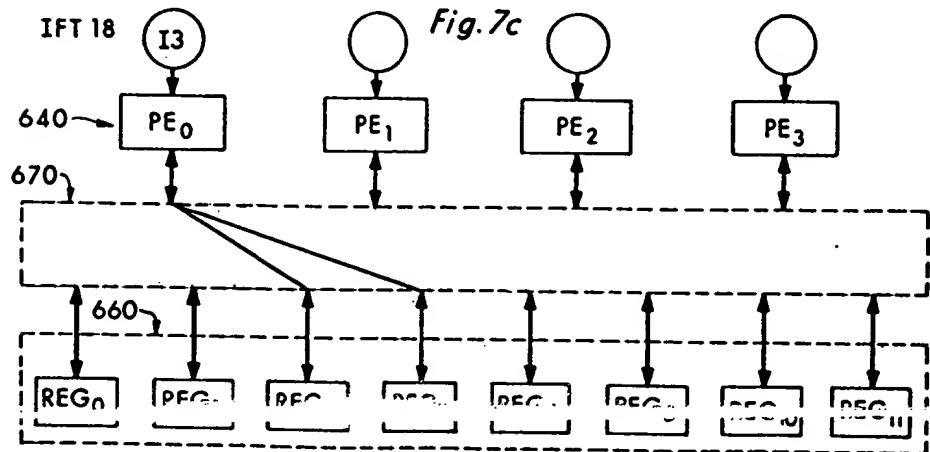
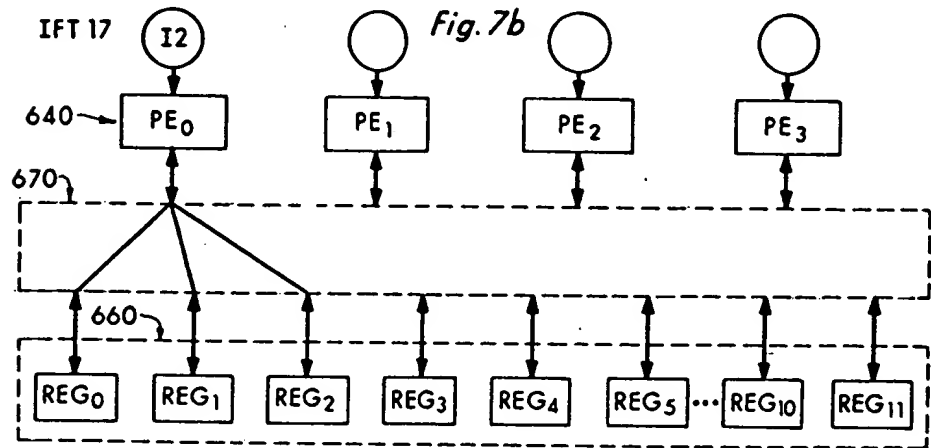
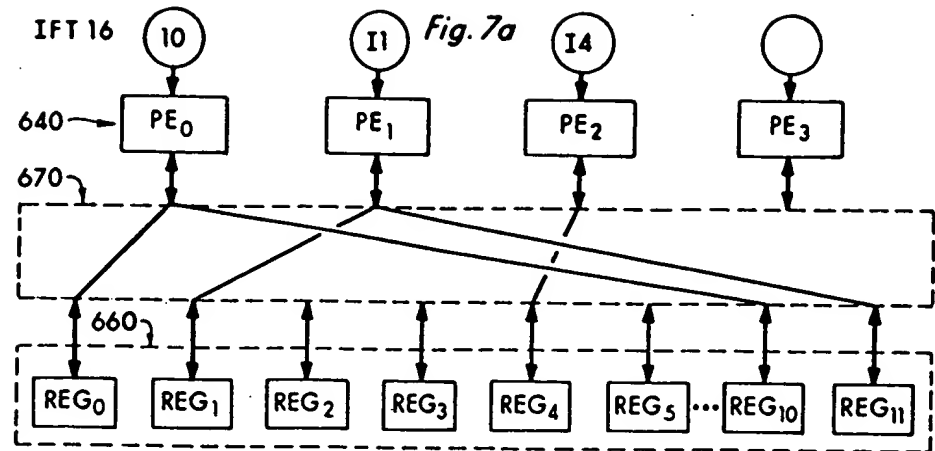


Fig. 6



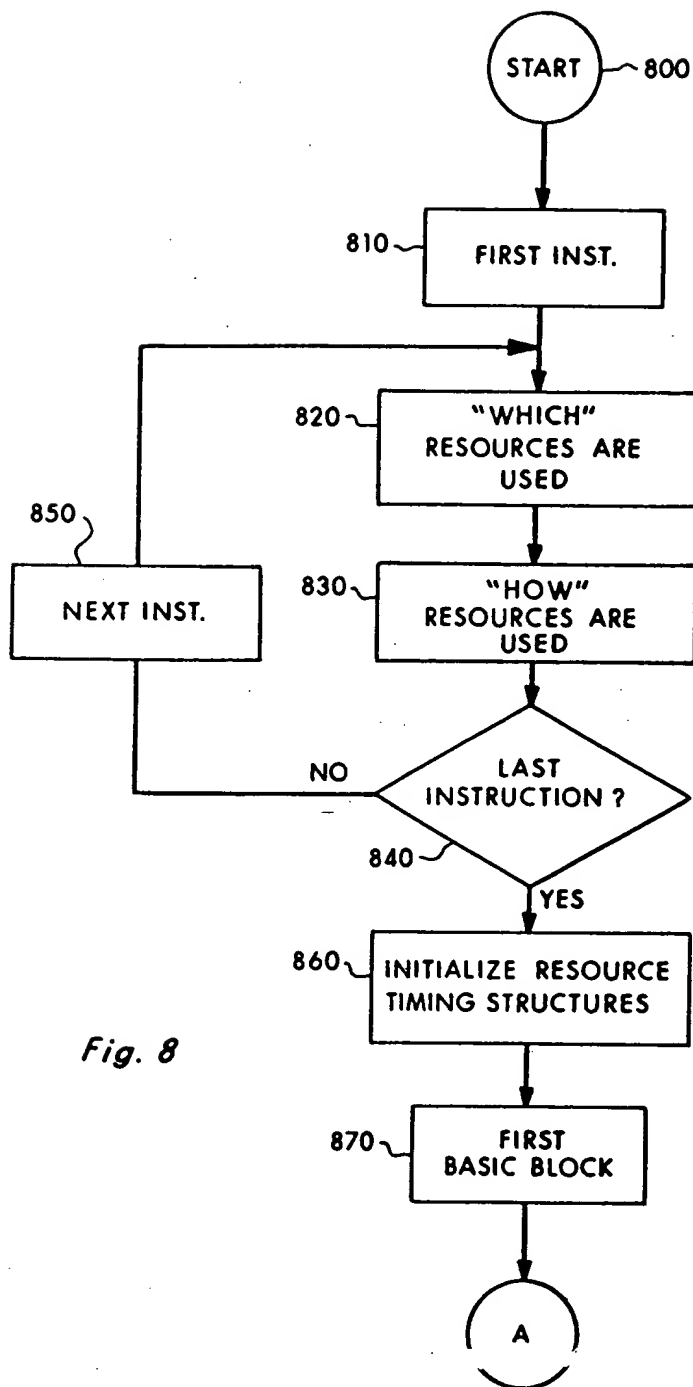
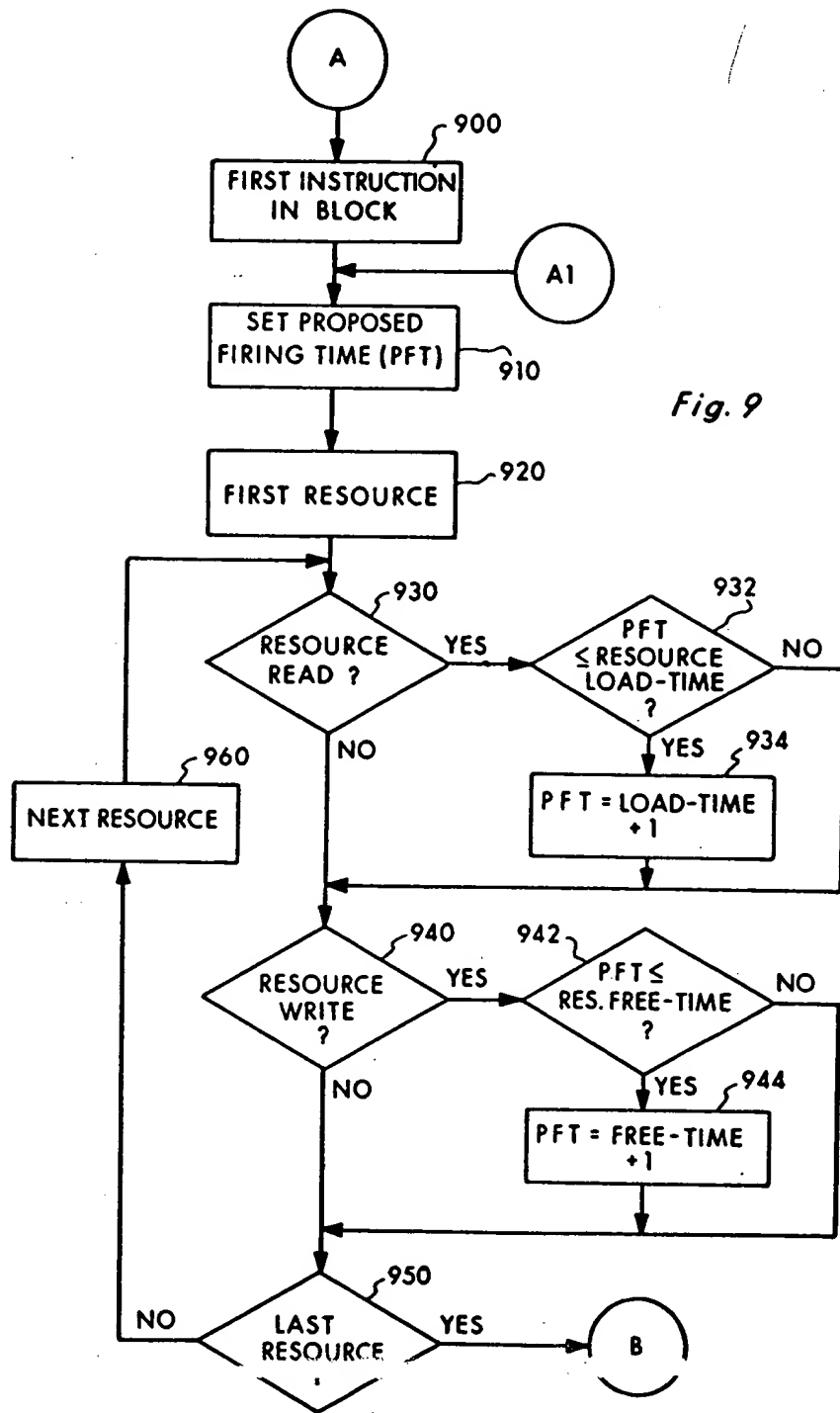


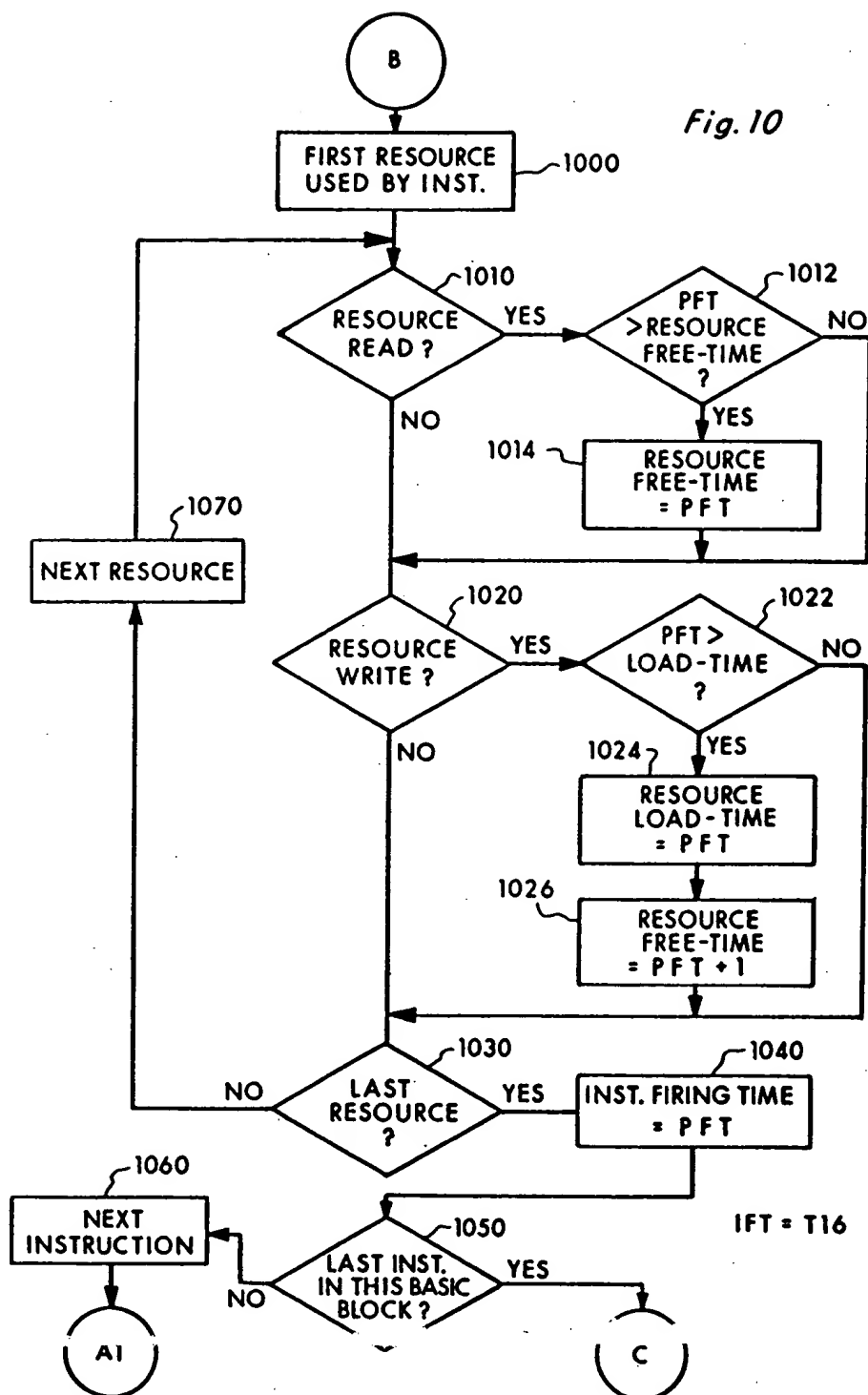
Fig. 8

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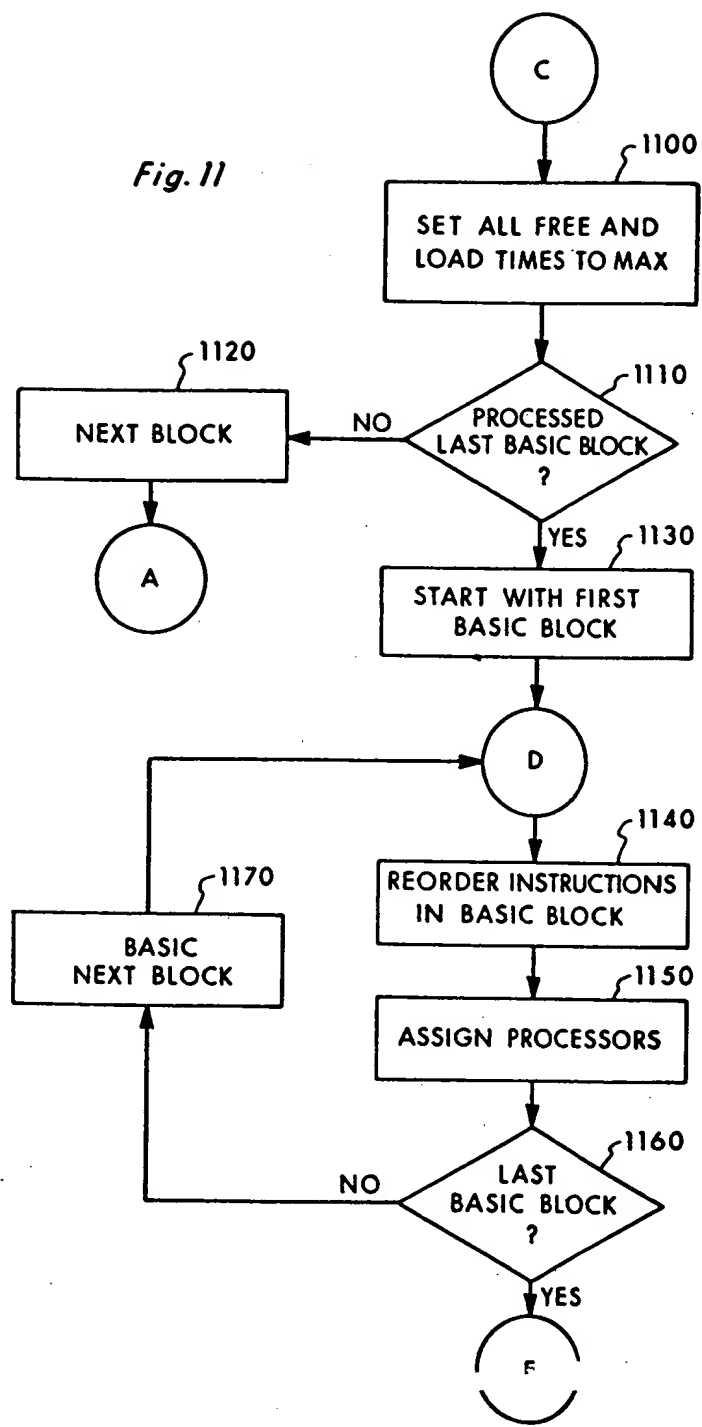
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Fig. 10



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Fig. 11



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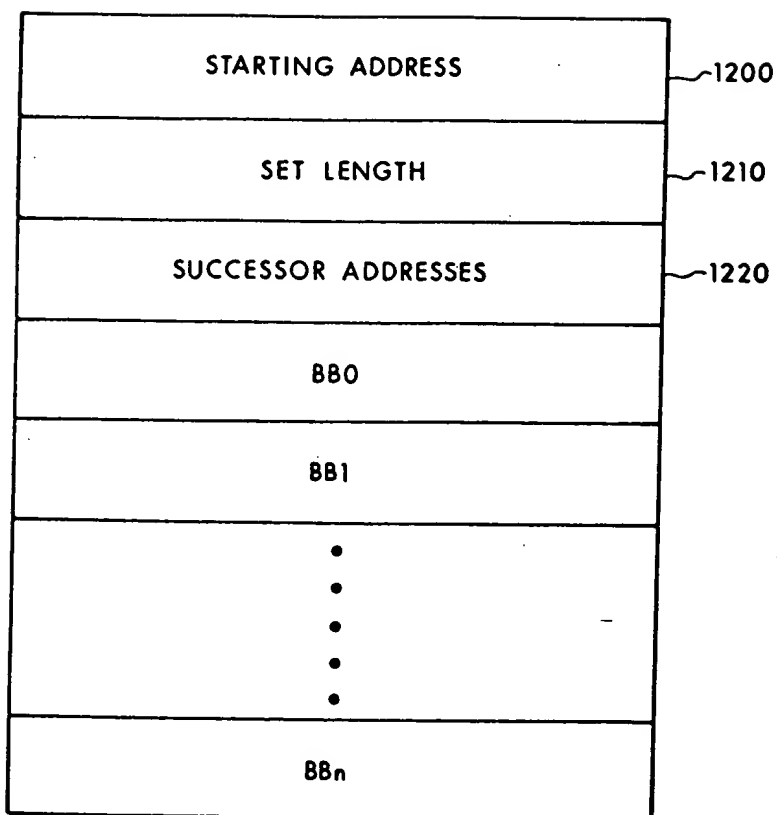


Fig. 12

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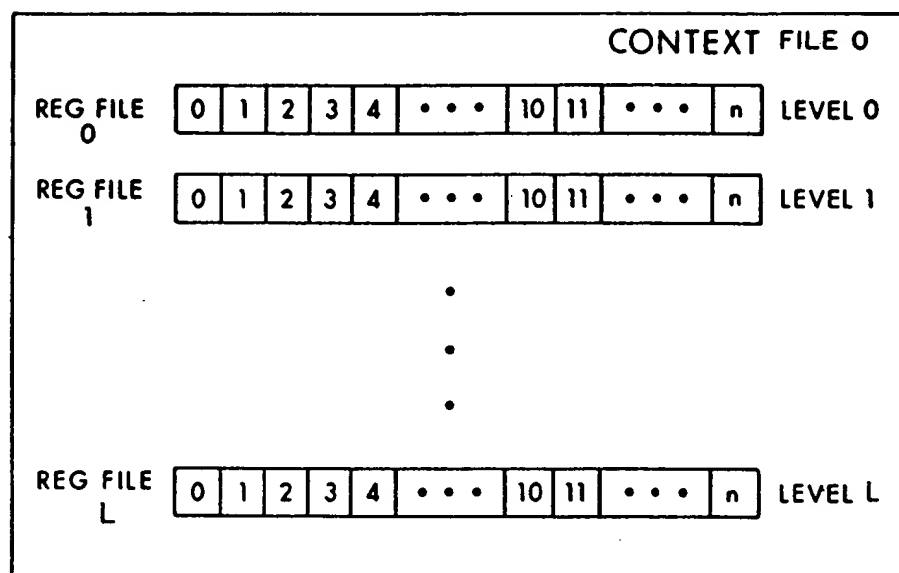


Fig. 13

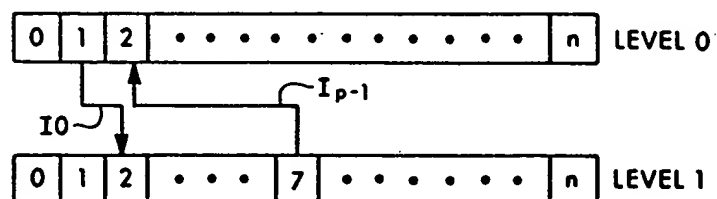


Fig. 14

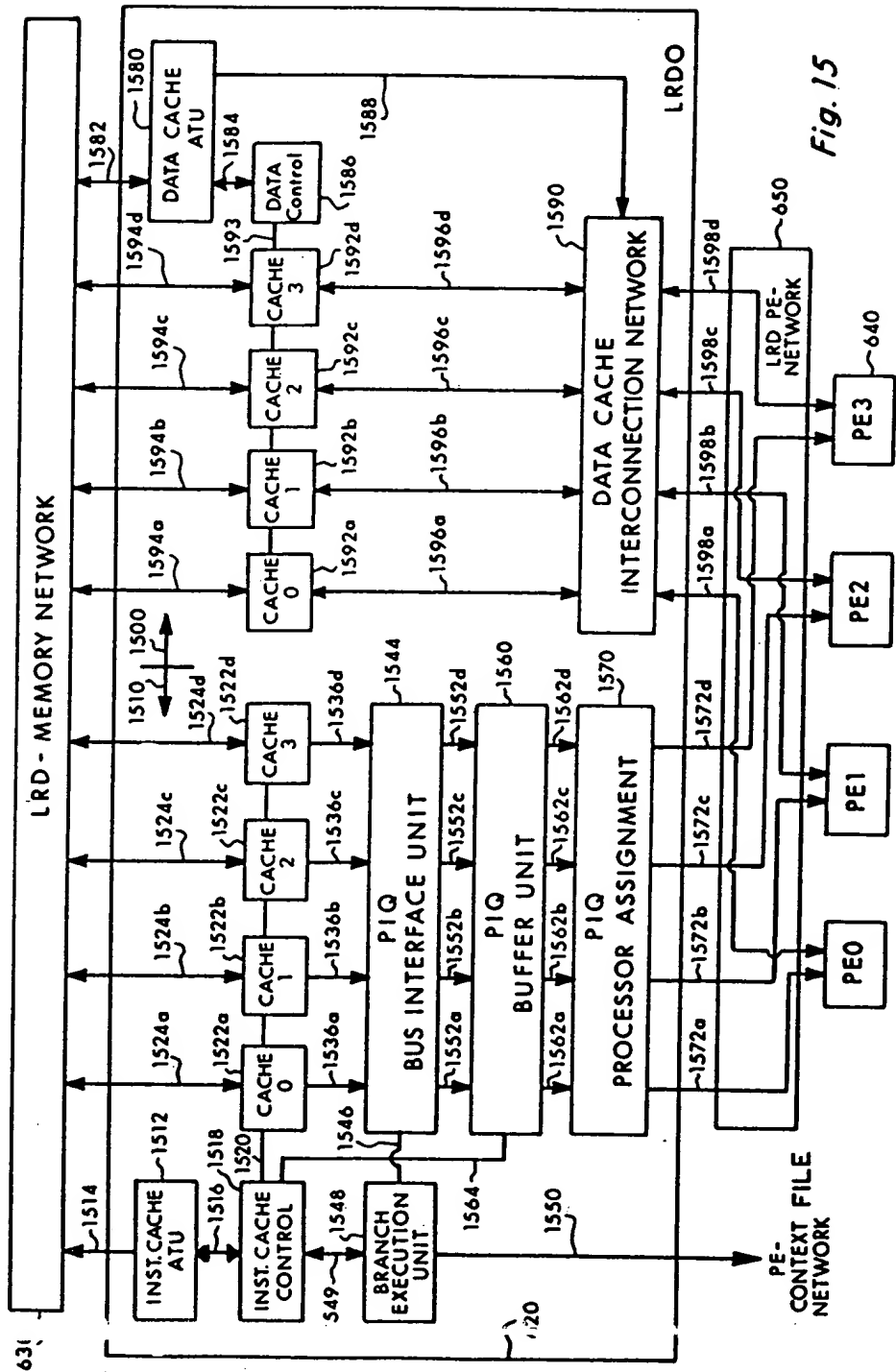


Fig. 15

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Fig. 16

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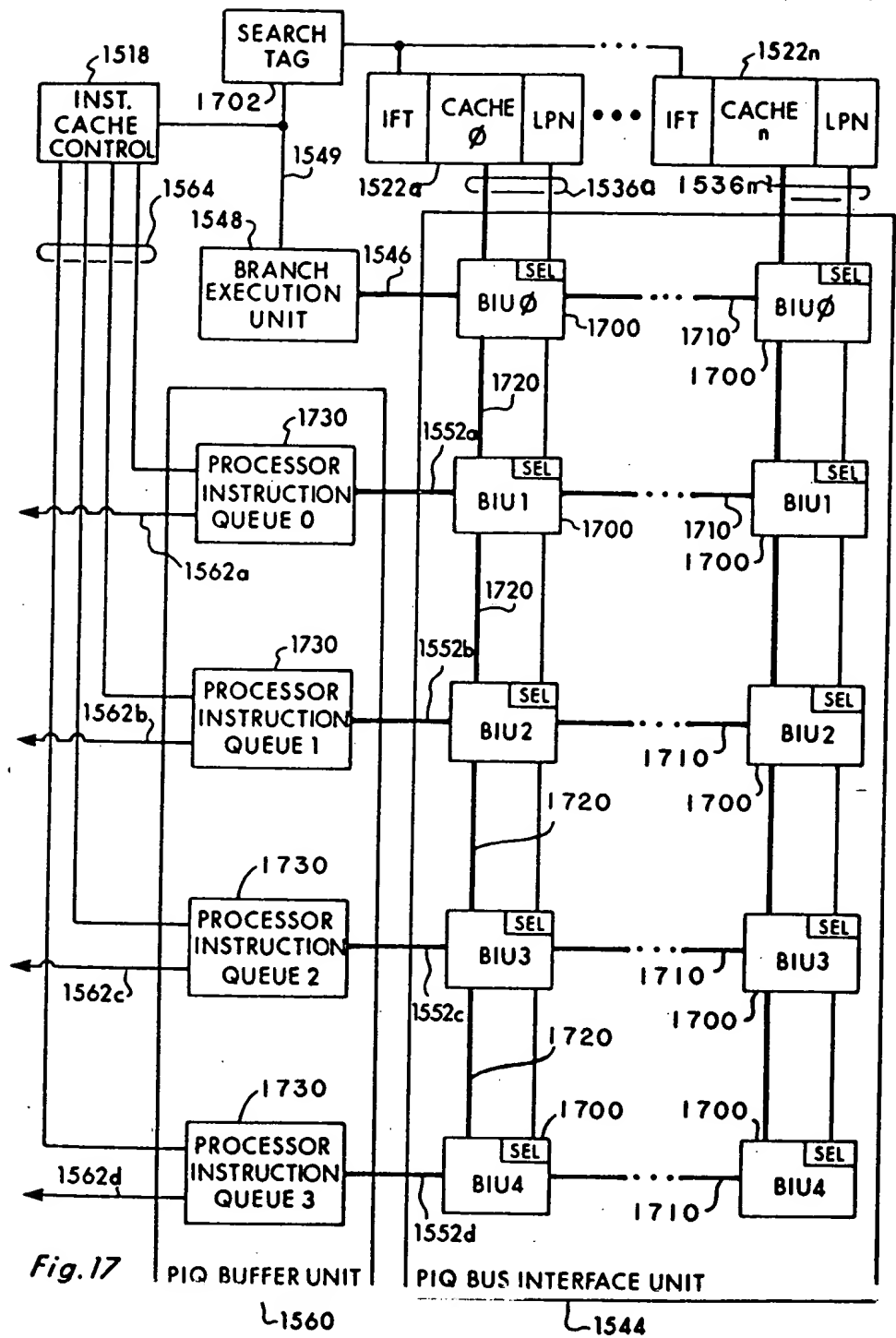
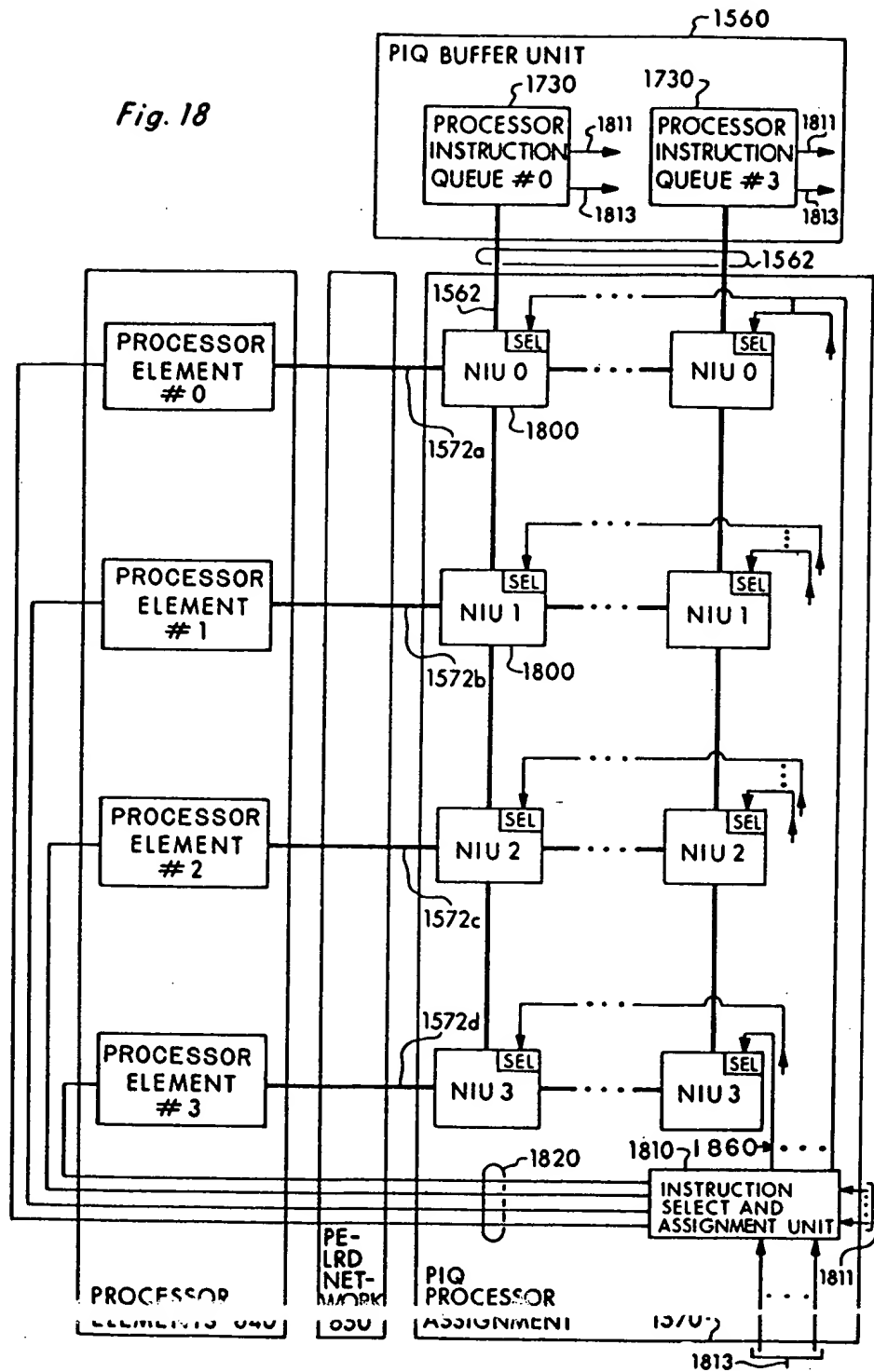


Fig. 17

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Fig. 18



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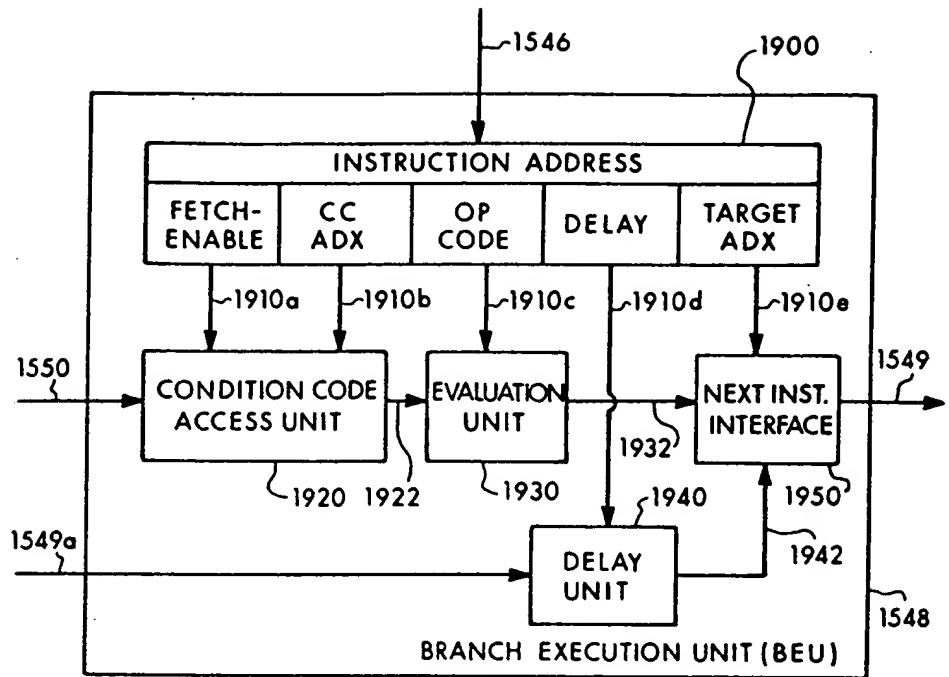


Fig. 19

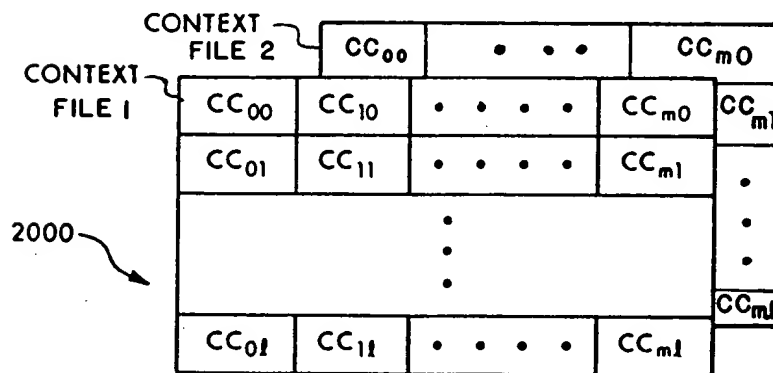
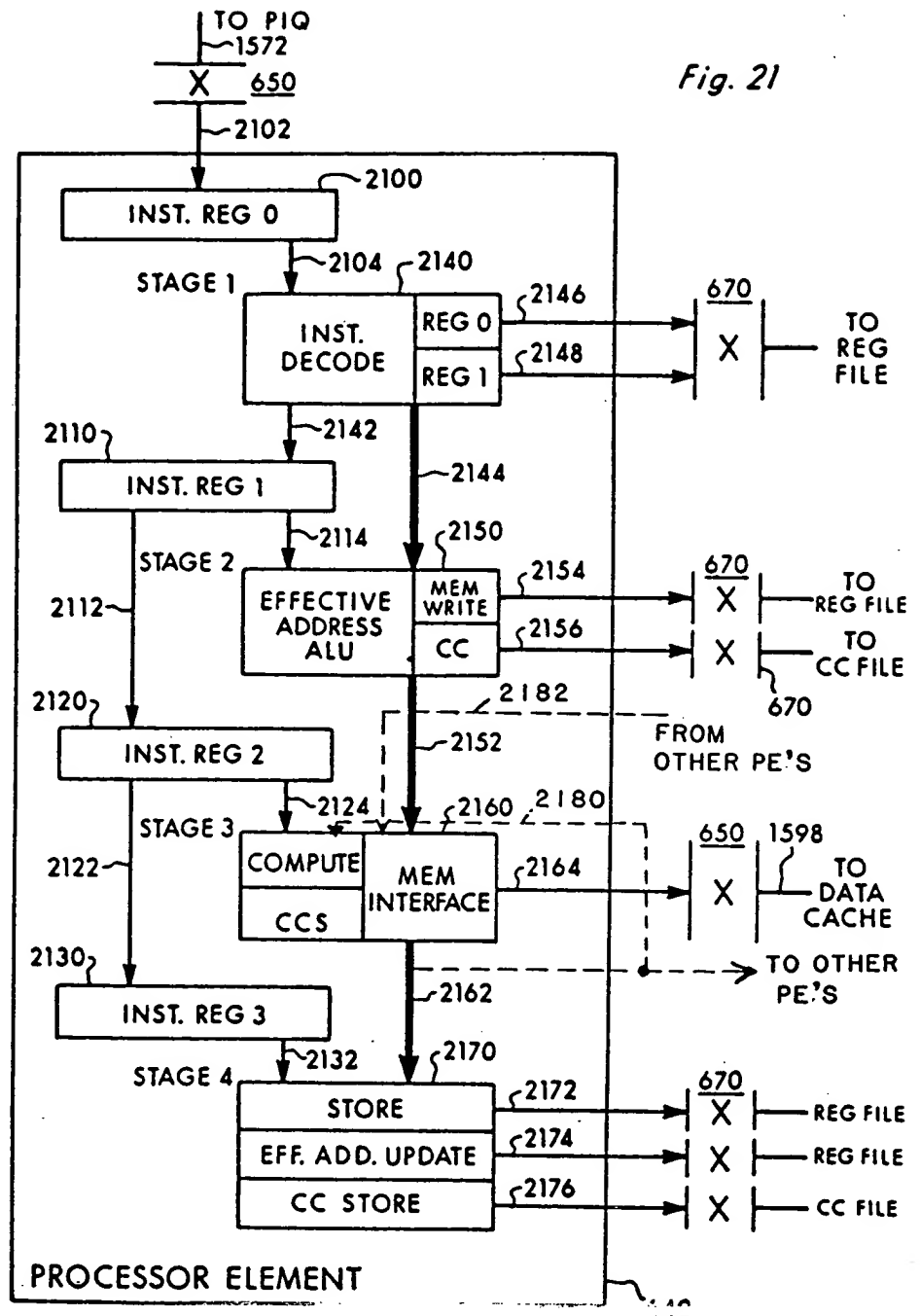


Fig. 20

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Fig. 21



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Fig. 22a

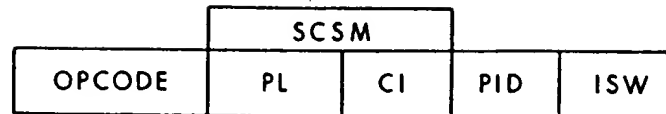


Fig. 22b

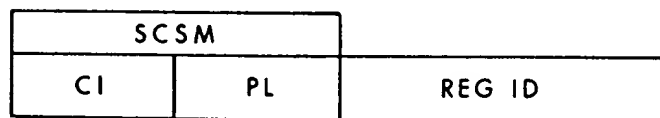


Fig. 22c



Fig. 22d

